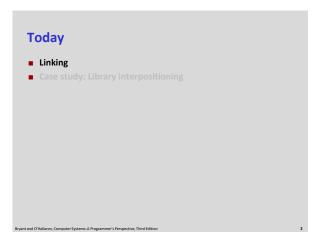
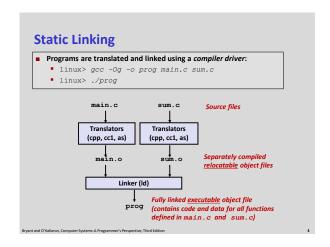
## Linking CSci 2021: Machine Architecture and Organization December 7th-10th, 2018 Your instructor: Stephen McCamant Based on slides originally by: Randy Bryant, Dave O'Hallaron



```
| int sum(int *a, int n); | int array[2] = {1, 2}; | int main() { | int val = sum(array, 2); | return val; } | main.c | | sum.c | | sum.c | su
```



# Why Linkers? Reason 1: Modularity Program can be written as a collection of smaller source files, rather than one monolithic mass. Can build libraries of common functions (more on this later) e.g., Math library, standard C library

# Why Linkers? (cont) Reason 2: Efficiency Time: Separate compilation Change one source file, compile, and then relink. No need to recompile other source files. Space: Libraries Common functions can be aggregated into a single file... Yet executable files and running memory images contain only code for the functions they actually use.

## What Do Linkers Do?

- Step 1: Symbol resolution
  - Programs define and reference symbols (global variables and functions):
    - void swap() {...} /\* define symbol swap \*/
       swap(); /\* reference symbol swap \*/
       int \*xp = &x; /\* define symbol xp, reference x \*/
  - Symbol definitions are stored in object file (by assembler) in symbol table.
    - Symbol table is an array of structs
    - Each entry includes name, size, and location of symbol.
  - During symbol resolution step, the linker associates each symbol reference with exactly one symbol definition.

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## What Do Linkers Do? (cont)

- Step 2: Relocation
  - Merges separate code and data sections into single sections
  - Relocates symbols from their relative locations in the . o files to their final absolute memory locations in the executable.
  - Updates all references to these symbols to reflect their new positions.

Let's look at these two steps in more detail....

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## **Three Kinds of Object Files (Modules)**

- Relocatable object file (.o file)
  - Contains code and data in a form that can be combined with other relocatable object files to form executable object file.
    - Each .  $\circ$  file is produced from exactly one source ( .  $\circ$  ) file
- Executable object file (a.out file)
  - Contains code and data in a form that can be copied directly into memory and then executed.
- Shared object file (. so file)
  - Special type of relocatable object file that can be loaded into memory and linked dynamically, at either load time or run-time.
  - Called Dynamic Link Libraries (DLLs) by Windows

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## **Executable and Linkable Format (ELF)**

- Standard binary format for object files
- One unified format for
  - Relocatable object files (.o),
  - Executable object files (a.out)
  - Shared object files (.so)
- Generic name: ELF binaries

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## **ELF Object File Format**

- Elf header
  - Word size, byte ordering, file type (.o, exec, .so), machine type, etc.
- Segment header table
  - Page size, virtual addresses memory segments (sections), segment sizes.
- .textse
  - Code
- .rodata section
  - Read only data: jump tables, ...
- . data section
  - Initialized global variables
- .bss section
  - Uninitialized global variables
  - "Block Started by Symbol"
  - "Better Save Space"
- Has section header but occupies no space

Segment header table (required for executables)

. text section

. rodata section

. data section

.bss section

.symtab section

.rel.txt section

.rel.data section

.debug section

Section header table

## **ELF Object File Format (cont.)**

- . symtab section
  - Symbol table
  - Procedure and static variable names
  - Section names and locations
- .rel.textsection
- Relocation info for .text section
- Addresses of instructions that will need to be modified in the executable
- Instructions for modifying.
- .rel.datasection
  - Relocation info for .data section
  - Addresses of pointer data that will need to be modified in the merged executable
- debug section
- Info for symbolic debugging (gcc -g)
- Section header table
- Offsets and sizes of each section

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ELF header
Segment header table (required for executables)
. text section
.rodatasection
. data section
. bss section
.symtab section
.rel.txt section
.rel.data section
. debug section
Section header table

2

## **Linker Symbols**

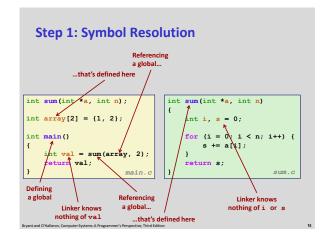
### Global symbols

- Symbols defined by module m that can be referenced by other modules.
- E.g.: non-static C functions and non-static global variables.

## External symbols

- Global symbols that are referenced by module m but defined by some other module.
- Local symbols
  - Symbols that are defined and referenced exclusively by module *m*.
  - E.g.: C functions and global variables defined with the static attribute.
  - Local linker symbols are not local program variables

.....



## **Local Symbols**

- Local non-static C variables vs. local static C variables
  - local non-static C variables: stored on the stack
  - local static C variables: stored in either .bss, or .data

int f()
{
 static int x = 0;
 return x;
}
int g()
{
 static int x = 1;
 return x;
}

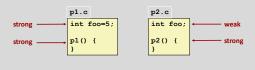
Compiler allocates space in .data for each definition of x

Creates local symbols in the symbol table with unique names, e.g.,  $\mathbf{x}$  . 1 and  $\mathbf{x}$  . 2.

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## How Linker Resolves Duplicate Symbol Definitions

- Program symbols are either strong or weak
  - Strong: procedures and initialized globals
  - Weak: uninitialized globals

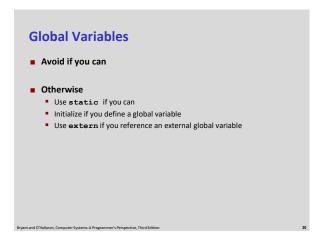


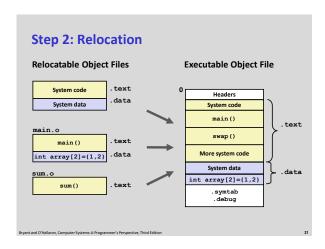
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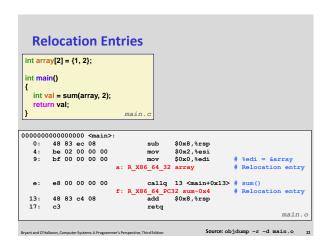
## **Linker's Symbol Rules**

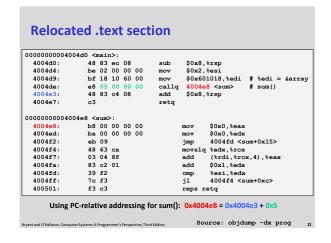
- Rule 1: Multiple strong symbols are not allowed
  - Each item can be defined only once
  - Otherwise: Linker error
- Rule 2: Given a strong symbol and multiple weak symbols, choose the strong symbol
  - References to the weak symbol resolve to the strong symbol
- Rule 3: If there are multiple weak symbols, pick an arbitrary one
  - Can override this with gcc -fno-common

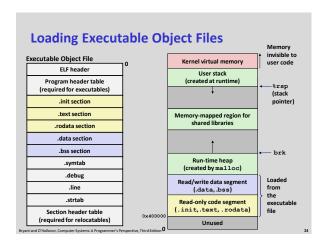
### **Linker Puzzles** Link time error: two strong symbols (p1) p1() {} int x; p1() {} int x; p2() {} References to x will refer to the same uninitialized int. Is this what you really want? double x; p2() {} Writes to x in p2 might overwrite y! int y; p1() {} int x=7; double x; Writes to $\mathbf{x}$ in $\mathbf{p2}$ will overwrite $\mathbf{y}$ ! int y=5; p1() {} p2() {} Nasty! References to x will refer to the same initialized int x=7; p1() {} int x; p2() {} Nightmare scenario: two identical weak structs, compiled by different compilers with different alignment rules.





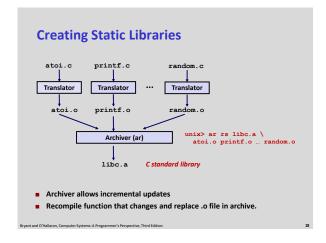


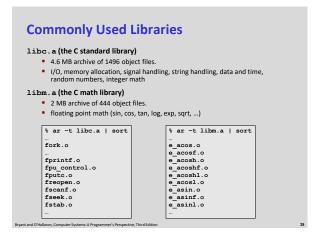


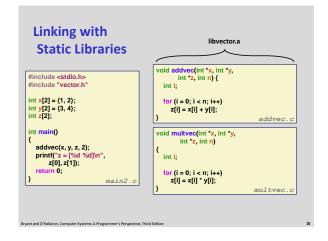


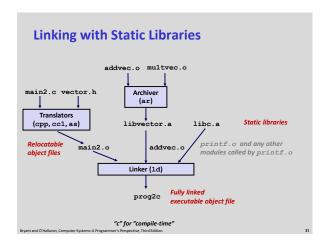
## Packaging Commonly Used Functions How to package functions commonly used by programmers? Math, I/O, memory management, string manipulation, etc. Awkward, given the linker framework so far: Option 1: Put all functions into a single source file Programmers link big object file into their programs Space and time inefficient Option 2: Put each function in a separate source file Programmers explicitly link appropriate binaries into their programs More efficient, but burdensome on the programmer

## Static libraries (.a archive files) Concatenate related relocatable object files into a single file with an index (called an archive). Enhance linker so that it tries to resolve unresolved external references by looking for the symbols in one or more archives. If an archive member file resolves reference, link it into the executable.









```
Using Static Libraries

■ Linker's algorithm for resolving external references:

■ Scan . o files and . a files in the command line order.

■ During the scan, keep a list of the current unresolved references.

■ As each new . o or . a file, obj, is encountered, try to resolve each unresolved reference in the list against the symbols defined in obj.

■ If any entries in the unresolved list at end of scan, then error.

■ Problem:

■ Command line order matters!

■ Moral: put libraries at the end of the command line.

| unix> gcc -L. libtest.o -lmine unix> gcc -L. -lmine libtest.o libtest.o: In function 'main': libtest.o: In function 'main': libtest.o(.text+0x4): undefined reference to 'libfun'
```

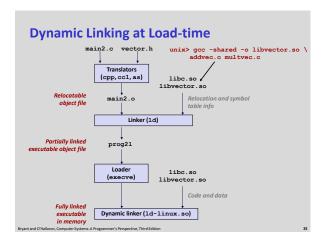
## Modern Solution: Shared Libraries

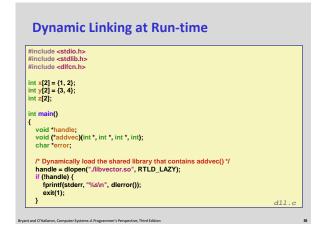
- Static libraries have the following disadvantages:
  - Duplication in the stored executables (every function needs libc)
  - Duplication in the running executables
  - Minor bug fixes of system libraries require each application to explicitly relink
- Modern solution: Shared Libraries
  - Object files that contain code and data that are loaded and linked into an application dynamically, at either load-time or run-time
  - Also called: dynamic link libraries, DLLs, .so files

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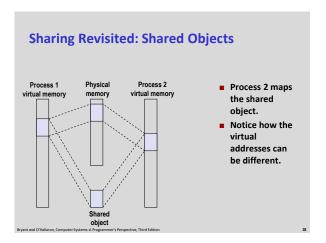
## **Shared Libraries (cont.)**

- Dynamic linking can occur when executable is first loaded and run (load-time linking).
  - Common case for Linux, handled automatically by the dynamic linker (ld-linux.so).
  - Standard C library (libc.so) usually dynamically linked.
- Dynamic linking can also occur after program has begun (run-time linking).
  - In Linux, this is done by calls to the **dlopen()** interface.
    - · Distributing software.
    - · High-performance web servers.
    - Runtime library interpositioning.
- Shared library routines can be shared by multiple processes.
  - Using mechanisms we discussed under virtual memory





# Dynamic Linking at Run-time ... /\* Get a pointer to the addvec() function we just loaded \*/ addvec = dlsym(handle, \*addvec"); if ((error = dlerrof()) != NULL) { fprintf(stder, \*%s\n', error); exit(1); } /\* Now we can call addvec() just like any other function \*/ addvec(x, y, z, 2); printf(\*z = [%d %d[\n', z[0], z[1]); /\* Unload the shared library \*/ if (dlclose(handle) < 0) { fprintf(stder, \*%s\n', dlerror()); exit(1); } return 0; } All.c



## **Position Independent Code**

- Requirement
  - Shared library code may be loaded at different addresses in different processes, must still run correctly
- Solution for direct jumps: PC relative
  - Target of calls and jumps is encoded as a relative offset, so works correctly if source and target move together
- Solution for local data: also PC relative
  - Offset between code and data areas is fixed at compilation time
  - Use %rip as base address
    - E.g., mov 0x20047d(%rip), %eax
  - Add the displacement to the address of the next instruction

## **GOT and PLT**

- How about accesses between modules, like between main program and a shared library?
- Indirect through Global Offset Table (GOT)
  - GOT contains absolute addresses of code and data
  - Offset between PC and GOT is known at static linking time, but GOT contents updated at runtime
  - Adds one extra level of indirection to accesses

## **Local and GOT data access examples**

Source code in a shared library:

```
static long addcnt = 0; /* in this file */
extern int error; /* in another library */
void addvec(..) { ...
    addcnt++;
    error = 0;
}
```

Assembly code for addcnt++:

```
5f5: mov 0x200a24(%rip), %rax # 201020 <addcnt>
5fc: add $0x1, %rax
600: mov %rax, 0x200a19(%rip) # 201020 <addcnt>
```

Assembly code for error = 0:

607: mov 0x2009c2(%rip), %rax # 200fd0 GOT entry 60e: movl \$0x0, (%rax)

## **Procedure Lookup Table**

- Used for calls to functions in a shared library
  - Address determined lazily at first use
  - Indirection is transparent to the caller

```
00400420 <PLT[0]>:
400420: pushq 0x200bca(%rip) # 600ff0 <GOT[1]>
400426: jmpq *0x200bca(%rip) # 600ff8 <GOT[2]>
400426: nopl 0x0(%rax)

00400430 <printf@plt>:
400430: jmpq *0x200bca(%rip) # 601000 <GOT[3]>
400436: pushq $0x0
40043b: jmpq 400420 <PLT[0]>

00400440 <addvec@plt>:
400440: jmpq *0x200bca(%rip) # 601008 <GOT[4]>
400446: pushq $0x1
400446: pushq $0x1
400446: jmpq 400420 <PLT[0]>
```

## **Address Space Layout Randomization**

- Recall: defense to make attacks more difficult
  - Idea: choose random locations for memory areas
  - Attacker has to guess, modify attack, or leak information
- ASLR for stack and heap is easy
- ASLR for code and data depends on PIC
  - Always done for shared libraries on modern systems
- ASLR for the main program is optional
  - Compiling main program PIC = PIE
    - "Position Independent Exectutable"
  - Would slow down 32-bit x86 due to register use
  - Done for security-critical programs

## **Linking Summary**

- Linking is a technique that allows programs to be constructed from multiple object files.
- Linking can happen at different times in a program's lifetime:
  - Compile time (when a program is compiled)
  - Load time (when a program is loaded into memory)
  - Run time (while a program is executing)
- Understanding linking can help you avoid nasty errors and make you a better programmer.

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## **Today**

- Linking
- Case study: Library interpositioning

## **Case Study: Library Interpositioning**

- Library interpositioning: powerful linking technique that allows programmers to intercept calls to arbitrary functions
- Interpositioning can occur at:
  - Compile time: When the source code is compiled
  - Link time: When the relocatable object files are statically linked to form an executable object file
  - Load/run time: When an executable object file is loaded into memory, dynamically linked, and then executed.

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## **Some Interpositioning Applications**

- Security
  - Confinement (sandboxing)
  - Behind the scenes encryption
- Debugging
  - In 2014, two Facebook engineers debugged a treacherous 1-year old bug in their iPhone app using interpositioning
  - Code in the SPDY networking stack was writing to the wrong location
  - Solved by intercepting calls to Posix write functions (write, writev, pwrite)

Source: Facebook engineering blog post at https://code.facebook.com/posts/313033472212144/debugging-file-corruption-on-ios/

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## **Some Interpositioning Applications**

- Monitoring and Profiling
  - Count number of calls to functions
  - Characterize call sites and arguments to functions
  - Malloc tracing
    - Detecting memory leaks
    - Generating address traces

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## **Example program**

```
#include <stdio.h>
#include <malloc.h>
int main()
{
   int 'p = malloc(32);
   free(p);
   return(0);
}
```

- Goal: trace the addresses and sizes of the allocated and freed blocks, without breaking the program, and without modifying the source code.
- Three solutions: interpose on the lib malloc and free functions at compile time, link time, and load/run time.

## #define malloc(size) mymalloc(size) #define malloc(size) mymalloc(size) #define free(ptr) myfree(ptr) void \*mymalloc(size t size); void myfree(void \*ptr); malloc.h linux> make intc gcc -Wall -DCOMPILETIME -c mymalloc.c gcc -Wall -I. -o intc int.c mymalloc.o linux> make runc ./intc malloc(32)=0xledc010 free(0xledc010) linux>

```
#ifdef LINKTIME
#include <stdio.h>

void *_real_malloc(size_t size);
void __real_free(void *ptr);

/* malloc wrapper function */
void *_wrap_malloc(size_t size)
{
    void *ptr = __real_malloc(size); /* Call libc malloc */
    printf("malloc(%d) = %p\n", (int)size, ptr);
    return ptr;
}

/* free wrapper function */
void __wrap_free(void *ptr)
{
    __real_free(ptr); /* Call libc free */
    printf("free(%p)\n", ptr);
}
#endif

mymalloc.c
```

```
Link-time Interpositioning

Linux> make intl
gcc -Wall -DLINKTIME -c mymalloc.c
gcc -Wall -c int.c
gcc -Wall -wl, --wrap, malloc -Wl, --wrap, free -o intl
int.o mymalloc.o
linux> make runl
./intl
malloc(32) = 0xlaa0010
free(0xlaa0010)
linux>

The "-Wl" flag passes argument to linker, replacing each
comma with a space.

The "--wrap, malloc" arg instructs linker to resolve
references in a special way:

Refs to malloc should be resolved as malloc
Refs to __real_malloc should be resolved as malloc
```

```
#iddef RUNTIME
#define_GNU_SOURCE
#define_GNU_SOURCE
#include_estdio.hs
#include_estdib.hs
#include_edficn.hs

/* malloc wrapper function */
void *malloc(size_t size)
{

void *(*mallocp)(size_t size);
char *error;

mallocp = dlsym(RTLD_NEXT, *malloc"); /* Get addr of libc malloc */
if ((error = dlerror(j)) != NULL) {
    fputs(error, stderr);
    exit(1);
}
char *ptr = mallocp(size); /* Call libc malloc */
printf(*malloc(%d) = %p\n*, (int)size, ptr);
return ptr;
}

mymalloc.c
```

```
| free wrapper function */
void free(void *ptr) {
    void (*freep)(void *) = NULL;
    char *error;
    if (lptr)
        return;
    freep = dlsym(RTLD_NEXT, *free*); /* Get address of libc free */
    if ((error = dlerror()) != NULL) {
        fputs(error, stderr);
        exit(1);
    }
    freep(ptr); /* Call libc free */
    printf(*free(%p))n*, ptr);
    }
    #endif

    mymalloc.c
```

```
Load/Run-time Interpositioning

linux> make intr
gcc -Wall -DRUNTIME -shared -fpic -o mymalloc.so mymalloc.c -ldl
gcc -Wall -o intr int.c
linux> make runr
(LD_PRELOAD="./mymalloc.so" ./intr)
malloc(32) = 0xe60010
free(0xe60010)
linux>

The LD_PRELOAD environment variable tells the dynamic
linker to resolve unresolved refs (e.g., to malloc) by looking
in mymalloc.so first.
```

## **Interpositioning Recap**

## ■ Compile Time

 Apparent calls to malloc/free get macro-expanded into calls to mymalloc/myfree

### Link Time

- Use linker trick to have special name resolutions
  - malloc → \_\_wrap\_malloc
  - \_\_real\_malloc → malloc

## ■ Load/Run Time

Implement custom version of malloc/free that use dynamic linking to load library malloc/free under different names

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**10**