Computer Science 2021 Section 010
Spring 2020
Midterm Exam 1
February 24th, 2020
Time Limit: 50 minutes, 3:35pm-4:25pm

- This exam contains 6 pages (including this cover page) and 4 questions. Once we tell you to start, please check that no pages are missing.
- Before starting the exam, you can fill out your name and other information of this page, but don't open the exam until you are directed to start. Don't put any of your answers on this page.
- You may use any textbooks, notes, or printouts you wish during the exam, but you may not use any electronic devices: no calculators, smart phones, laptops, etc.
- You may ask clarifying questions of the instructor or TAs, but no communication with other students is allowed during the exam.
- Please read all questions carefully before answering them. Remember that we can only grade what you write on the exam, so it's in your interest to show your work and explain your thinking.
- Students often find that the questions vary in difficulty. Your best strategy is usually to skim over all the questions, and then start working on the ones that look easiest. We also suggest that you leave time at the end to attempt every question, since we can't give you any partial credit if you leave a question blank.
- By signing below you certify that you agree to follow the rules of the exam, not to share exam material with other students before their exams, and that the answers on this exam are your own work only.

The exam will end promptly at 4:25pm. Good luck!

Your name (print): _____

Your UMN email/X.500: _____`@umn.edu`

Sign and date: _____

Question	Points	Score
1	20	
2	30	
3	30	
4	20	
Total:	100	

1. (20 points) Integer expressions in C.

   Below are 10 statements of C code that each assign an integer value between 1 and 10 to a variable `x`. In fact, each of the values 1 through 10 is used exactly once. Write the value that will be assigned to `x` in the blank next to each statement.

   You can assume that the integer `x`, the integer array `arr`, and the string `str` are declared as:

```
int x;
int arr[5] = {3, 1, 4, 1, 5};
char *str = "42";
```

   (a) _____ `x = 1 + 2 * 2 + 1;`

   (b) _____ `x = 8 ^ 1;`

   (c) _____ `x = 1 << 3;`

   (d) _____ `x = arr[0] + arr[2];`

   (e) _____ `x = str[1 + 1] + 1;`

   (f) _____ `x = str[1] - '2' + 2;`

   (g) _____ `x = 0xa0 >> 4;`

   (h) _____ `x = (0xa >> 1) & (-1 << 1);`

   (i) _____ `x = 4 | 1;`

   (j) _____ `x = ~(~0 << 2);`

2. (30 points) A string operation using C pointers.

   One way to reverse the order of characters in a string is to interchange each character in the first half of the string with a corresponding character in the second half: swapping the first character with the last character, the second character with the second-to-last character, and so on. For instance if you start with the string `AbyZ`, you could first swap `A` with `Z` to get `ZbyA`, and then swap `b` with `y` to get `ZybA`. In this question, your job is to finish implementing a C function that implements this general approach, using pointers.

   Part of the function has already been written, but you need to fill in the body of a loop that swaps characters using pointers and updates the pointers. You shouldn't need to declare any more variables, and you shouldn't use array syntax (like `[]` brackets) to access the characters. Just use the pointers `p` and `q` that have been initialized, and the temporary variable `temp` if necessary. Write one statement per blank line; you may not need to fill in every blank.

```
void reverse_str(char *str) {
    /* P points at the first (0th) character of the string */
    char *p = str;
    /* Q points at the last (non-null) character of the string */
    char *q = p + strlen(str) - 1;
    char temp;

    /* Repeat the loop while P points before Q */
    while (p < q) {
        /* Swap the characters pointed to by P and Q */

        _____________________________________________

        _____________________________________________

        _____________________________________________

        /* Move P and Q to the next characters to swap */

        _____________________________________________

        _____________________________________________

        _____________________________________________

    }
}
```

3. (30 points) Integer representations.

   Suppose we have a very small microprocessor with a 4-bit word size. A pattern of bits in one of its registers might be interpreted either as a signed (two's complement) integer or as an unsigned integer. Each row of the following table shows how one bit pattern can be interpreted as a signed or unsigned number. Given the various kinds of descriptions in the left column, your job is to fill in all the remaining table entries (we've done "five" as an example, and filled in a few other obvious ones). Remember that UMin, UMax, TMin and TMax refer to the smallest (Min) and largest (Max) representable unsigned (U) and signed (T) values. Some values may be repeated.

Description	Binary	Decimal (signed)	Decimal (unsigned)
zero			
five	0101	5	5
negative one		-1	
decimal 6		6	
unsigned 12			12
binary 1010	1010		
binary 0011	0011		
UMax			
TMin			
TMax			
TMin + TMin			
-TMax			

4. (20 points) Integer operations in assembly.

   Each of the x86-64 assembly-language code snippets shown on the left below eventually puts a fixed number into the `%rdi` register (the previous contents of the register don't matter). Match each sequence of instructions with the number it generates, chosen among the numbers show in the right-hand column, by writing the letter from the right in the blank. Numbers prefixed with `0x` are hexadecimal, while other numbers are decimal. Each number is used exactly once. Suggestion: don't try to do the arithmetic here by converting to decimal: the operations on hexadecimal values are all simpler if you keep them in binary/hexadecimal.

(a) ____
```
xorq %rdi, %rdi
```

(b) ____
```
movq $0, %rdi
leaq 4(%rdi), %rdi
```

(c) ____
```
movq $0x120, %rdi
xorq $0x100, %rdi
```

(d) ____
```
movq $0x20, %rdi
shrq $4, %rdi
```

(e) ____
```
movq $0x20, %rdi
leaq 0x60(%rdi,%rdi,4), %rdi
```

(f) ____
```
subq %rdi, %rdi
notq %rdi
andq $0x40, %rdi
```

(g) ____
```
movq $0x10, %rdi
leaq (,%rdi,8), %rdi
```

(h) ____
```
movq $0x50, %rdi
subq $0x40, %rdi
```

(i) ____
```
movq $1, %rdi
shlq $3, %rdi
```

(j) ____
```
movq $11, %rdi
xorq $10, %rdi
```

A. `0x0 (0)`

B. `0x1 (1)`

C. `0x2 (2)`

D. `0x4 (4)`

E. `0x8 (8)`

F. `0x10 (16)`

G. `0x20 (32)`

H. `0x40 (64)`

I. `0x80 (128)`

J. `0x100 (256)`

This extra page has a summary about x86-64 assembly language (AT&T format) for your reference:

Instructions	
`add` X, Y	add $X + Y$ and store in Y
`and` X, Y	compute bitwise AND of X and Y and store in Y
`lea` X, Y	compute address of X and store in Y
`mov` X, Y	copy value X to location Y
`not` X	compute bitwise complement of X and store in X
`shl` X, Y	shift Y left by X positions
`shr` X, Y	logical shift Y right by X positions
`sub` X, Y	compute $Y - X$ and store in Y
`xor` X, Y	compute bitwise XOR of X and Y and store in Y
Addressing modes	
(R)	mem[reg[R]]
D(R)	mem[D + reg[R]]
D(,R,S)	mem[D + reg[R] * S]
D(B,R,S)	mem[D + reg[B] + reg[R] * S]
Size suffixes	
b	8-bit byte
w	16-bit value
l	32-bit value
q	64-bit value