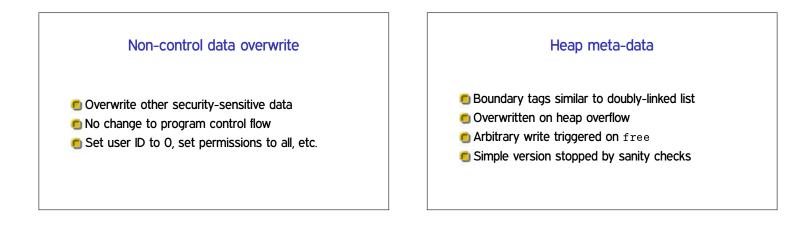
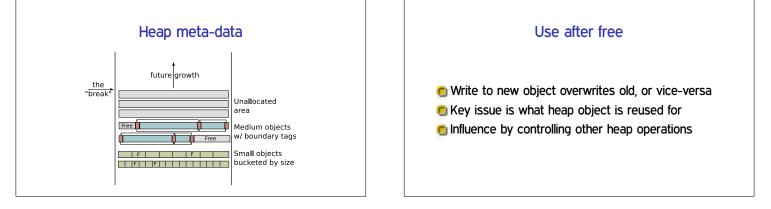
CSci 5271 Introduction to Computer Security Day 5: Low-level defenses and counterattacks

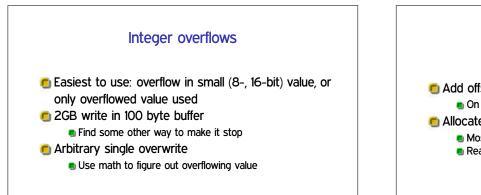
Stephen McCamant University of Minnesota, Computer Science & Engineering

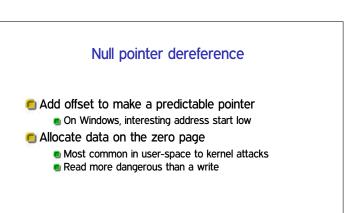
Outline

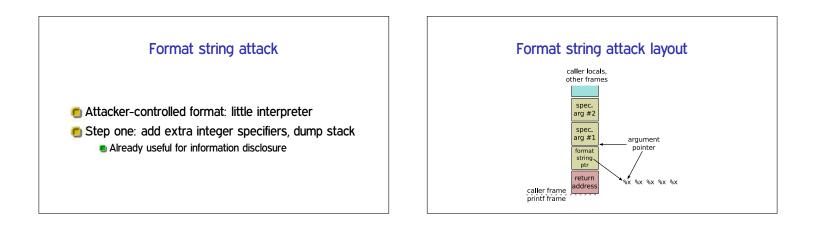
Exploiting other vulnerabilities Return address protections Announcements intermission ASLR and counterattacks $W \oplus X$ (DEP)

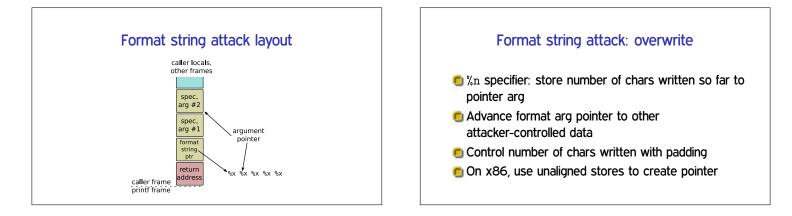


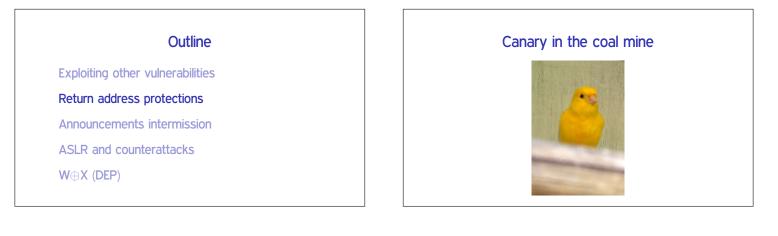


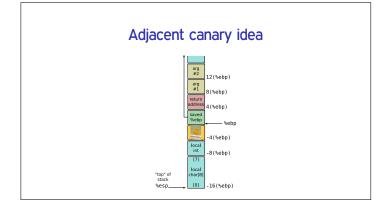


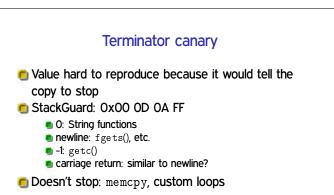


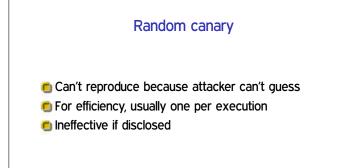






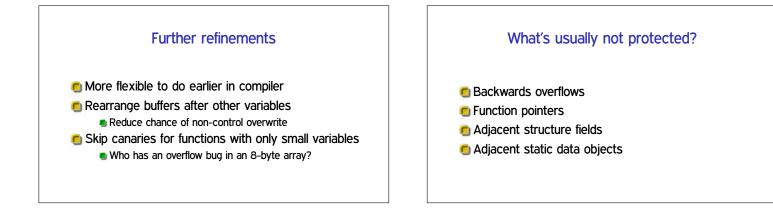






XOR canary

- Want to protect against non-sequential overwrites
- SOR return address with value c at entry
- XOR again with c before return
- Standard choice for c: see random canary



Where to keep canary value

Fast to access
 Buggy code/attacker can't read or write
 Linux/x86: %gs:0x14

Complex anti-canary attack

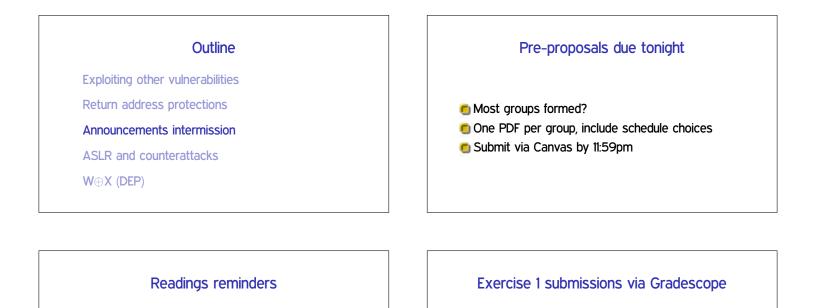
Canary not updated on fork in server
 Attacker controls number of bytes overwritten

Complex anti-canary attack

Canary not updated on fork in server
 Attacker controls number of bytes overwritten
 ANRY BNRY CNRY DNRY ENRY FNRY
 search 2³² → search 4 · 2⁸

Shadow return stack

- Suppose you have a safe place to store the canary
- Why not just store the return address there?
- Needs to be a separate stack
- Ultimate return address protection

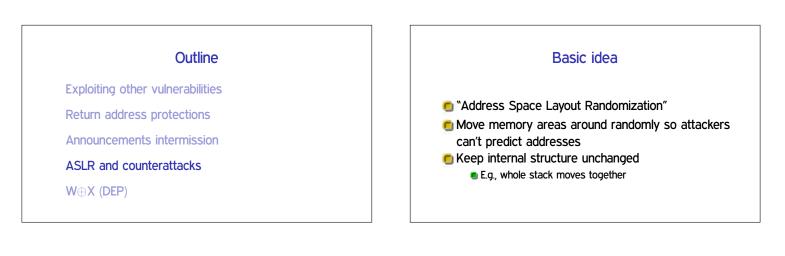


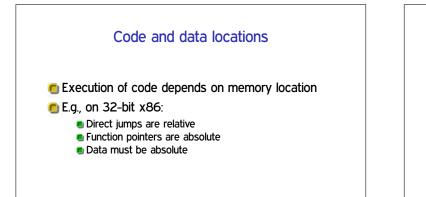
For last Wed.: buffer overflows and defenses
 For Monday: Attack techniques (under ASLR)

Solution Now also: academic (ACM) papers, campus/proxy

downloads

- Draft answers in plain text, submit in online text boxes
- Due a week from tonight, available now
- Bring your questions to Piazza or office hours





Relocation (Windows)

- Extension of technique already used in compilation
- Keep table of absolute addresses, instructions on how to update
- Disadvantage: code modifications take time on load, prevent sharing

PIC/PIE (GNU/Linux)

- "Position-Independent Code / Executable"
- Keep code unchanged, use register to point to data area
- Disadvantage: code complexity, register pressure hurt performance

What's not covered

- Main executable (Linux 32-bit PIC)
- Incompatible DLLs (Windows)
- Relative locations within a module/area

Entropy limitations

- Intuitively, entropy measures amount of randomness, in bits
- Random 32-bit int: 32 bits of entropy
- SLR page aligned, so at most 32 12 = 20 bits of entropy
- Other constraints further reduce possibilities

Leakage limitations

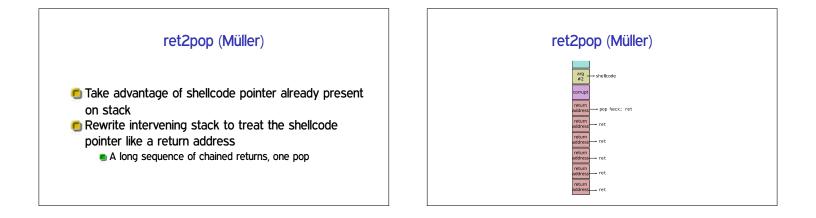
- If an attacker learns the randomized base address, can reconstruct other locations
- **Output** Stack address \rightarrow stack unprotected, etc.

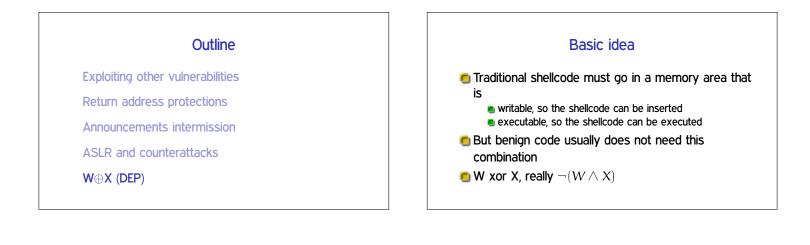
GOT hijack (Müller)

Main program fixed, libc randomized
 PLT in main program used to call libc
 Rewire PLT to call attacker's favorite libc functions
 E.g., turn printf into system

GOT hijack (Müller)

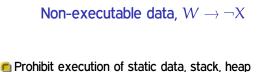
printf@plt: jmp *0x8049678 ... system@plt: jmp *0x804967c ... 0x8049678: <addr of printf in libc> 0x804967c: <addr of system in libc>







- E.g., read-only .text section
- Has been standard for a while, especially on Unix
- Lets OS efficiently share code with multiple program instances



- Not a problem for most programs
 - Incompatible with some GCC features no one uses
 Non-executable stack opt-in on Linux, but now near-universal

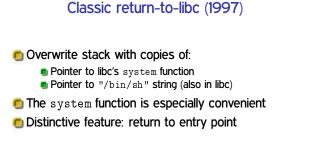
Implementing $W \oplus X$

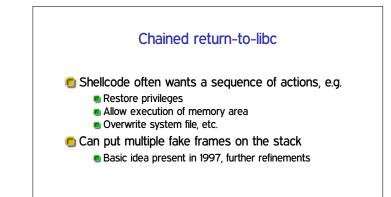
- Page protection implemented by CPU
 Some architectures (e.g. SPARC) long supported W
 X X X X X X
 - One bit controls both read and execute
 - Partial stop-gap "code segment limit"
- Eventual obvious solution: add new bit
 - NX (AMD), XD (Intel), XN (ARM)

One important exception

- Remaining important use of self-modifying code: just-in-time (JIT) compilers
 E.g., all modern JavaScript engines
- Allow code to re-enable execution per-block
 - 🍮 mprotect, Virtual Protect
 - Now a favorite target of attackers

Counterattack: code reuse Attacker can't execute new code So, take advantage of instructions already in binary There are usually a lot of them And no need to obey original structure





Beyond return-to-libc

- Can we do more? Oh, yes.
- Classic academic approach: what's the most we could ask for?
- Here: "Turing completeness"
- How to do it: reading for Monday

Next slides

Return-oriented programming (ROP) And counter-defenses

Control-flow integrity (CFI)